

**ABSTRACT OF THE DISCLOSURE**

The present invention provides a method and apparatus for dynamically configuring processor resources. The method includes processing tasks associated with at least one processor, wherein the processor comprises at least one cache memory having data stored therein. The method further includes transferring at least a portion of the data of the cache memory to another location and removing the processor from a system in response to transferring at least the portion of the data from the cache memory to another location, while the system is in operation.

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